IN THE CLAIMS:

Please amend the claims as follows:

1. (Currently Amended) An ~~IR memory for an~~ EGPRS receiver of a mobile station, ~~which receives~~ the EGPRS receiver configured to receive data from a base station via a data transmission channel and configured to measure a burst data transmission quality, the receiver comprising:

~~the~~ an IR memory having:

a) a first memory area ~~for buffer-storing~~ configured to buffer-store a specific number of data blocks with a predetermined first data resolution;

b) a second memory area ~~for buffer-storing~~ configured to buffer-store erroneously decoded data blocks[[,]];

c) the second memory area ~~storing~~ configured to store the erroneously decoded data blocks with a second data resolution, which is lower than the first data resolution[[,]]; ~~and~~

~~(d) it being possible for~~ wherein the second data resolution with which the erroneously decoded data blocks are stored in the second memory area of the IR memory ~~to be changed over~~ is configured to be set adaptively between different resolution levels ~~in a manner~~ dependent on a measured burst data transmission signal quality ~~measured by the receiver~~.

2. (Currently Amended) The ~~IR memory~~ EGPRS receiver as claimed in claim 1, wherein the first memory area of the IR memory is configured to store a [[the]]

number of data blocks ~~that can be stored in the first memory area of the IR memory depends on~~ dependent upon the internal signal delay within the mobile station.

3. (Currently Amended) The ~~IR memory~~ EGPRS receiver as claimed in claim 1, wherein the second memory area of the IR memory is configured to store a [[the]] number of data blocks ~~that can be stored in the second memory area of the IR memory depends on~~ dependent upon the polling period of the data transmission channel and on the round trip delay.

4. (Currently Amended) The ~~IR memory~~ EGPRS receiver as claimed in claim 1, wherein the resolution levels of the second data resolution ~~are~~ comprise 2 bits, 3 bits or 4 bits.

5. (Currently Amended) The ~~IR memory~~ EGPRS receiver as claimed in claim 1, wherein the first data resolution ~~are~~ comprises 5 bits.

6. (Currently Amended) The ~~IR memory~~ EGPRS receiver as claimed in claim 1, wherein the IR memory is connected, on the input side, to a reception buffer memory for data blocks.

7. (Currently Amended) The ~~IR memory~~ EGPRS receiver as claimed in claim 1, wherein the IR memory is connected to a decoder on the output side.

8. (Canceled)

9. (Canceled)